Page 3 of 6 - 08/976,805

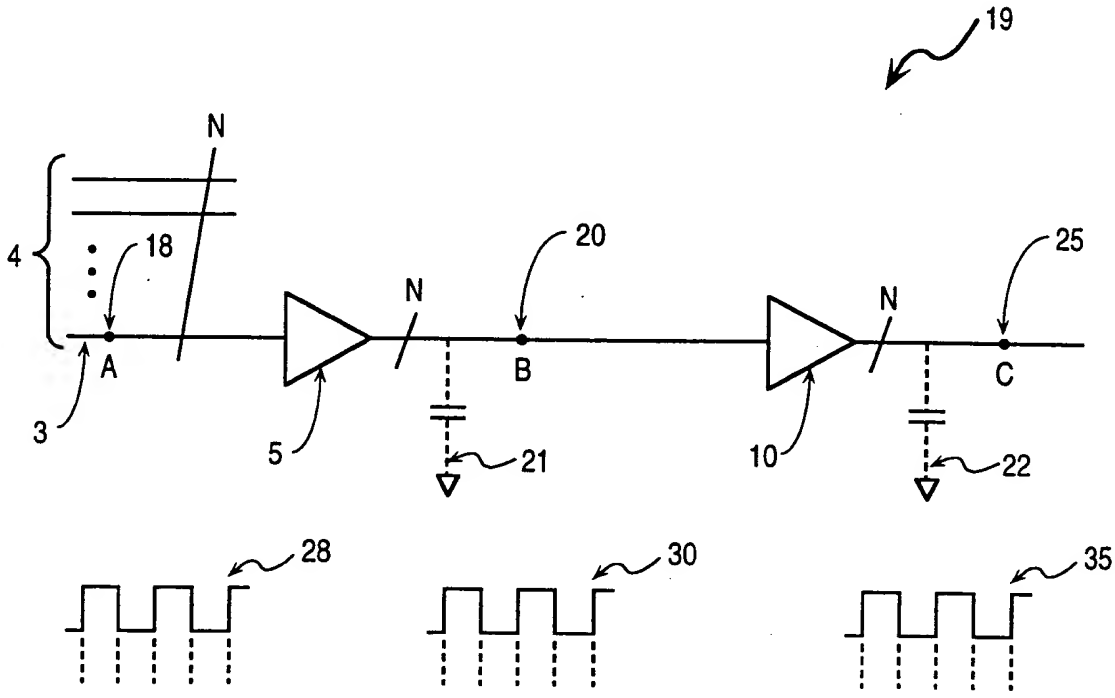


FIG. 1
(PRIOR ART)

FD7080 958h2660

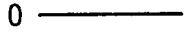
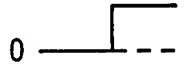
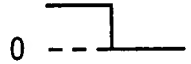
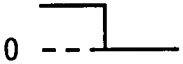
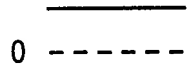
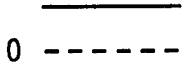
40 WAVEFORM AT "A"	42 NUMBER OF TOGGLES AT "A"	46 WAVEFORM AT "B"	48 NUMBER OF TOGGLES AT "B"	50 WAVEFORM AT "C"	52 NUMBER OF TOGGLES AT "C"	54 SUMMATION OF TOGGLES AT B & C Σ
	0		0		0	0
	1		1		1	2
	1		1		1	2
	0		0		0	0

FIG. 2
(PRIOR ART)

T07080 "958h2660

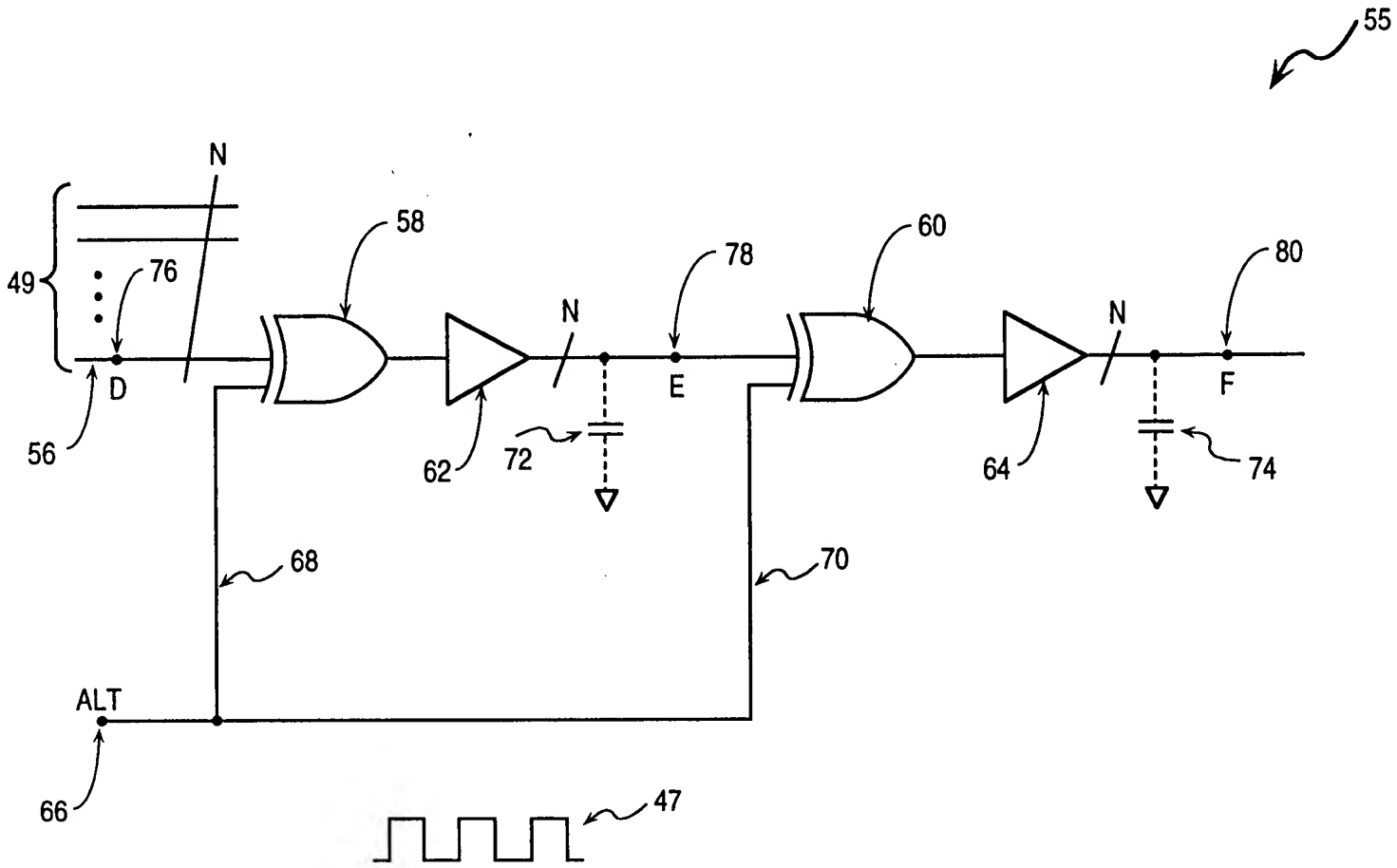


FIG. 3

TO2080 "958:2660

WAVEFORMS AT "D" (41)	NUMBER OF TOGGLES AT "D" (43)	WAVEFORMS AT "ALT" (45)	WAVEFORMS AT "E" (47)	NUMBER OF TOGGLES AT "E" (49)	WAVEFORMS AT "F" (51)	NUMBER OF TOGGLES AT "F" (53)	SUMMATION OF TOGGLES AT "E" & "F" Σ (73)
82							
0	0	0	0	1	0	0	1
0	1	0	0	0	0	1	1
0	1	0	0	0	0	1	1
0	0	0	0	1	0	0	1
84							
0	0	0	0	1	0	0	1
0	1	0	0	0	0	1	1
0	1	0	0	0	0	1	1
0	0	0	0	1	0	0	1

FIG. 4

T07080 "958h2660

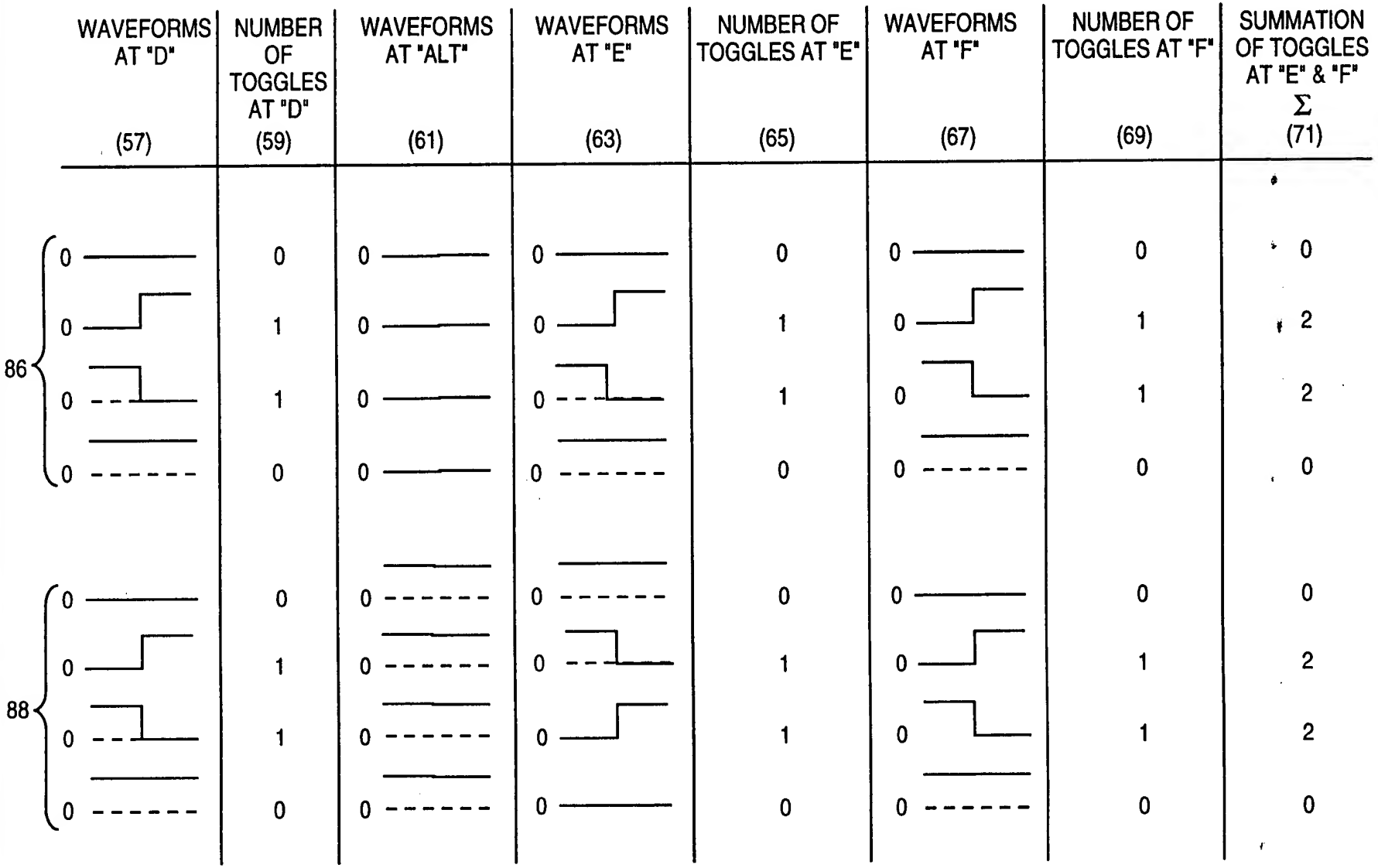


FIG. 5

PEAK SYSTEM POWER (APPROXIMATE)

	(90) WITHOUT ALT SIGNAL	(92) WITH ALT SIGNAL
• BEST CASE DATA PATTERN (91)	Ø WATTS	X/2 WATTS + Δ
• TYPICAL DATA PATTERN (93)	X/2 WATTS	X/2 WATTS + Δ
• WORST CASE DATA PATTERN (95)	X WATTS	X/2 WATTS + Δ

FIG. 6

0524855 - 0880502

T07080 "958h2660

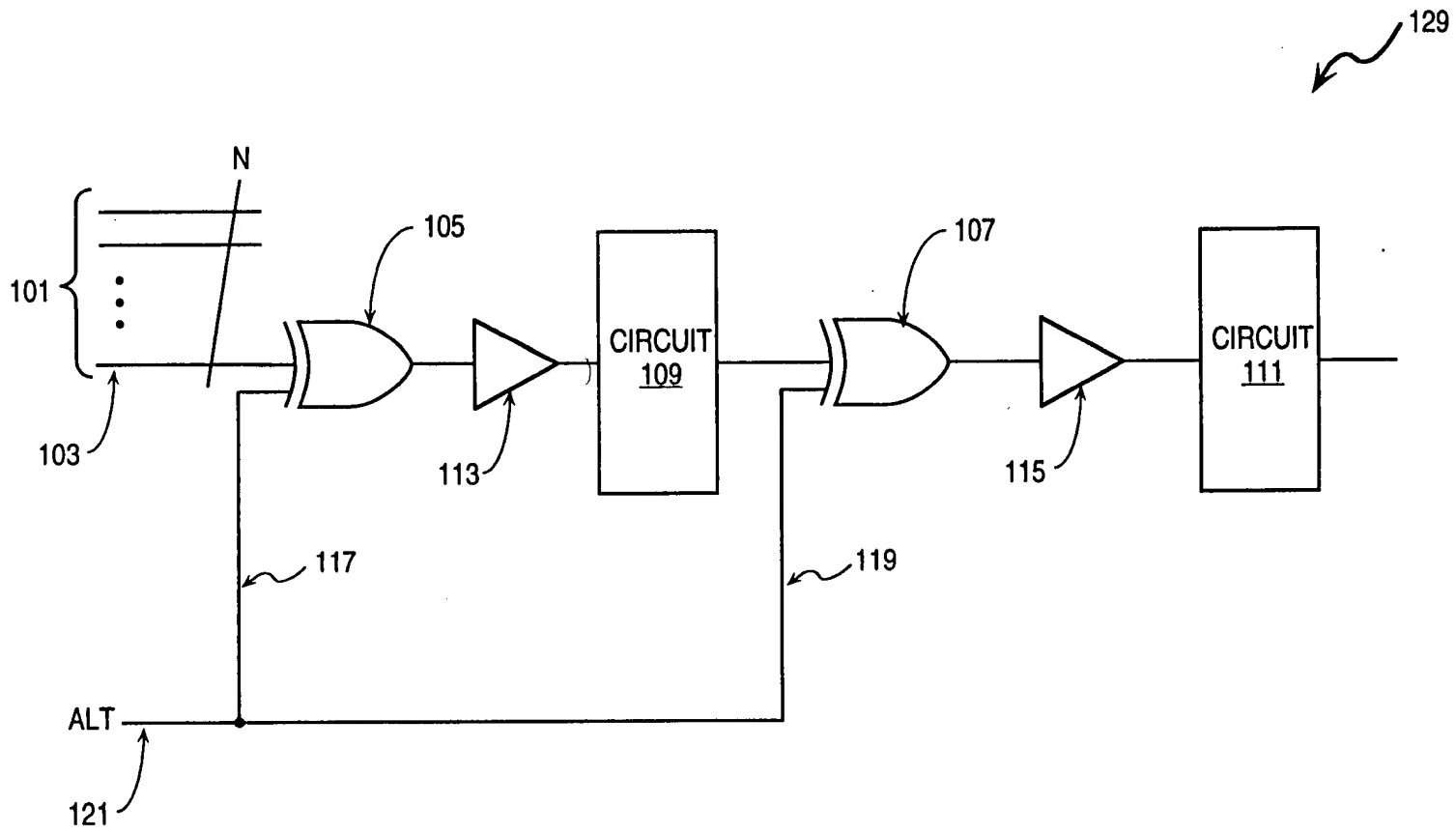


FIG. 7

T07080"958h2660

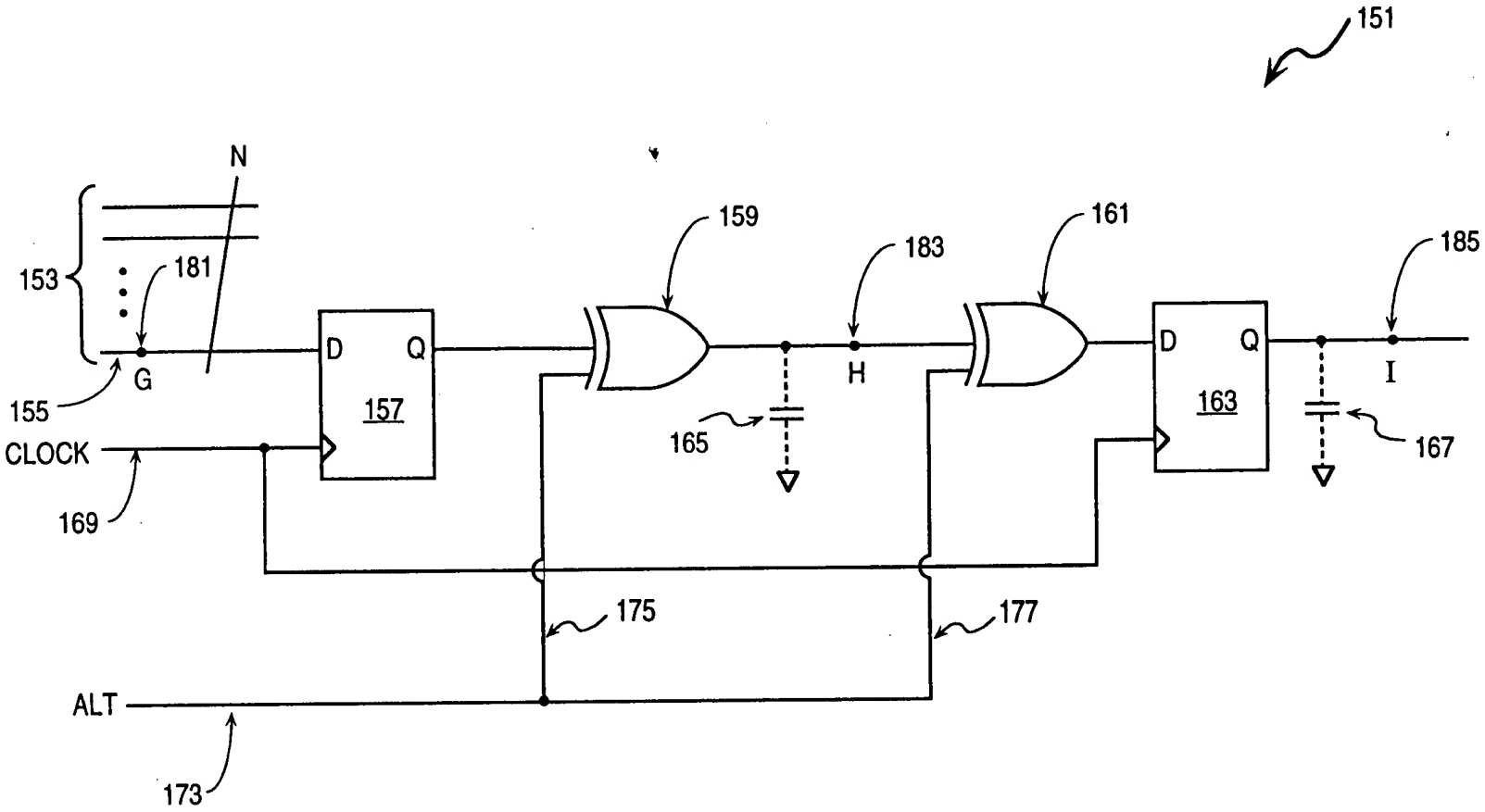


FIG. 8

T07080 "958H2660

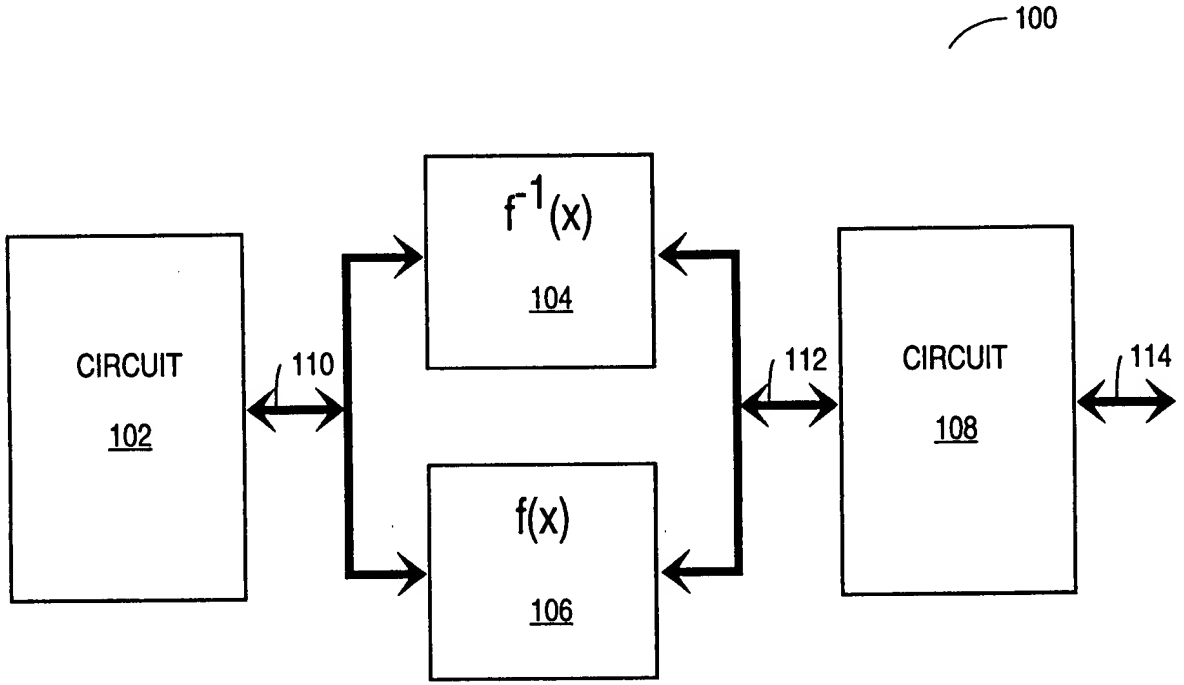


FIG. 9

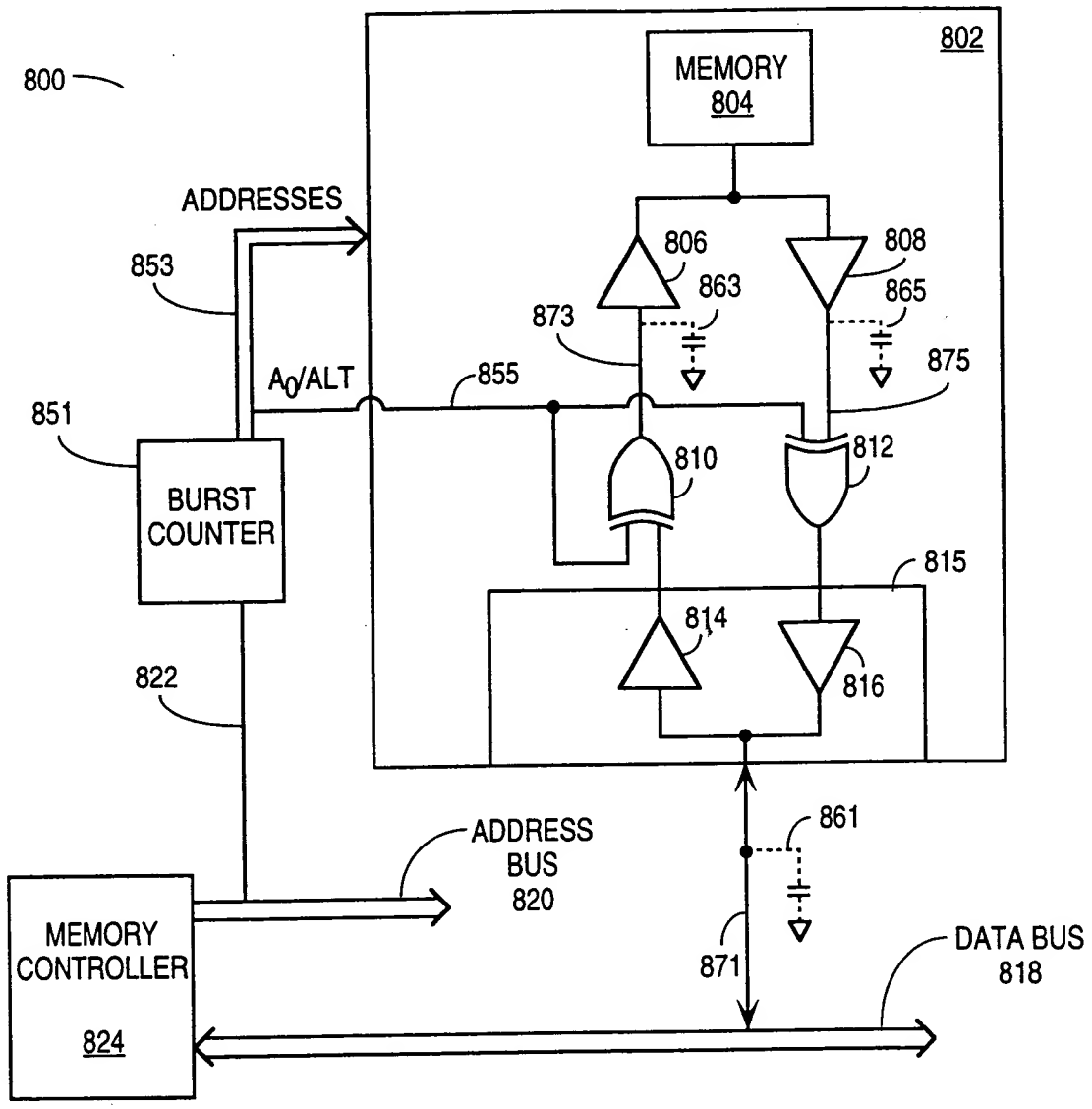

800
802
MEMORY
804
ADDRESSES
853
806
808
863
865
873
875
A_0/ALT
855
851
BURST
COUNTER
810
812
815
814
816
822
ADDRESS
BUS
820
861
MEMORY
CONTROLLER
824
871
DATA BUS
818

FIG. 10

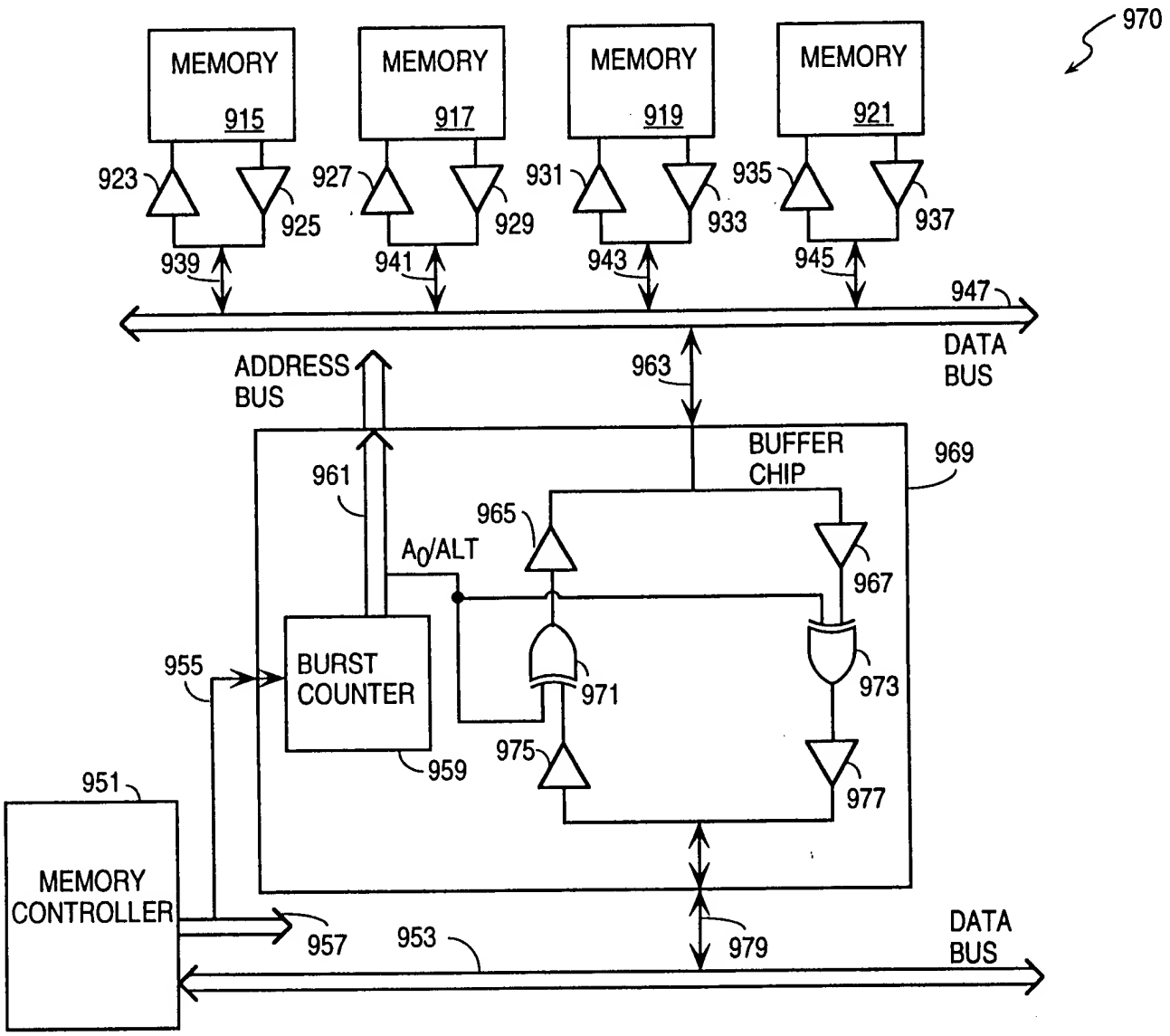

970
MEMORY
915
MEMORY
917
MEMORY
919
MEMORY
921
923
925
927
929
931
933
935
937
939
941
943
945
947
DATA
BUS
ADDRESS
BUS
963
BUFFER
CHIP
969
961
965
A0/ALT
967
955
BURST
COUNTER
971
973
975
959
977
951
MEMORY
CONTROLLER
957
953
979
DATA
BUS

FIG. 11

TO2080" 958h2660

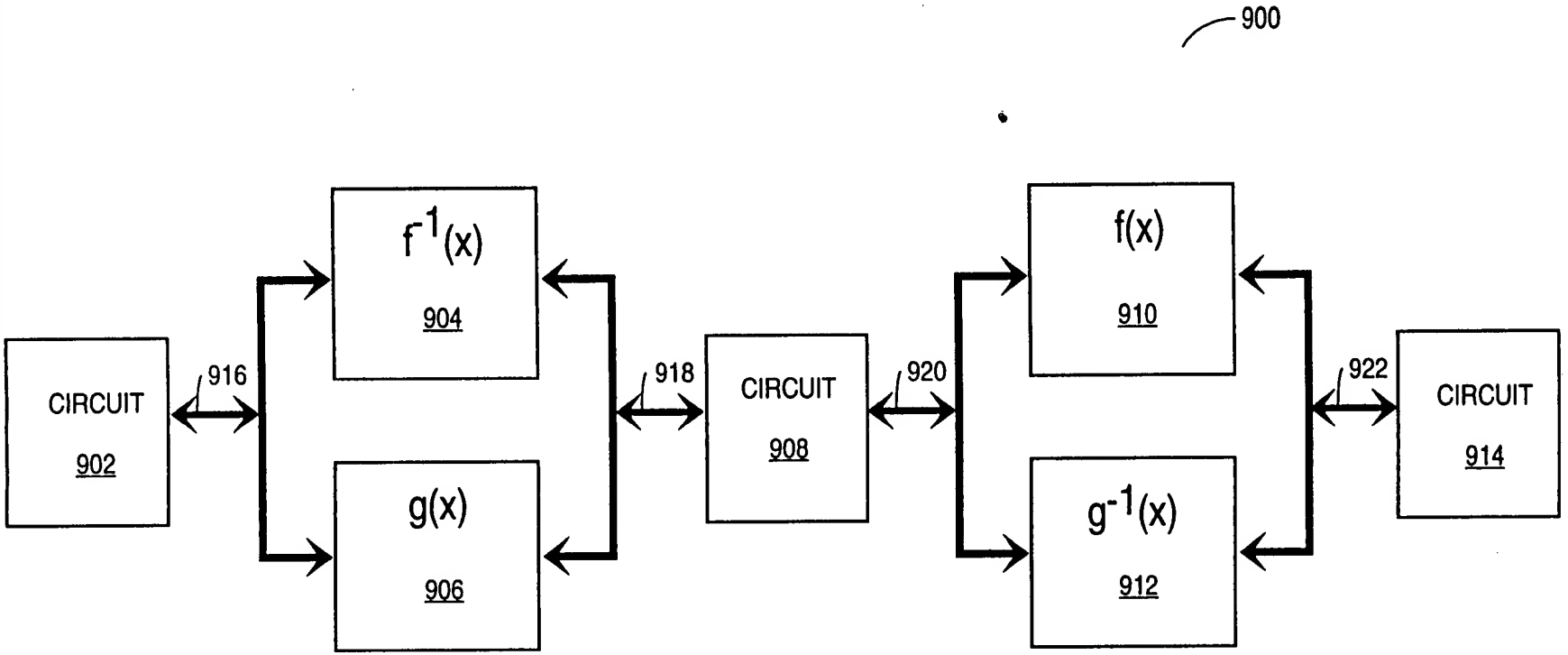


FIG. 12

TO2080 '958 h2660

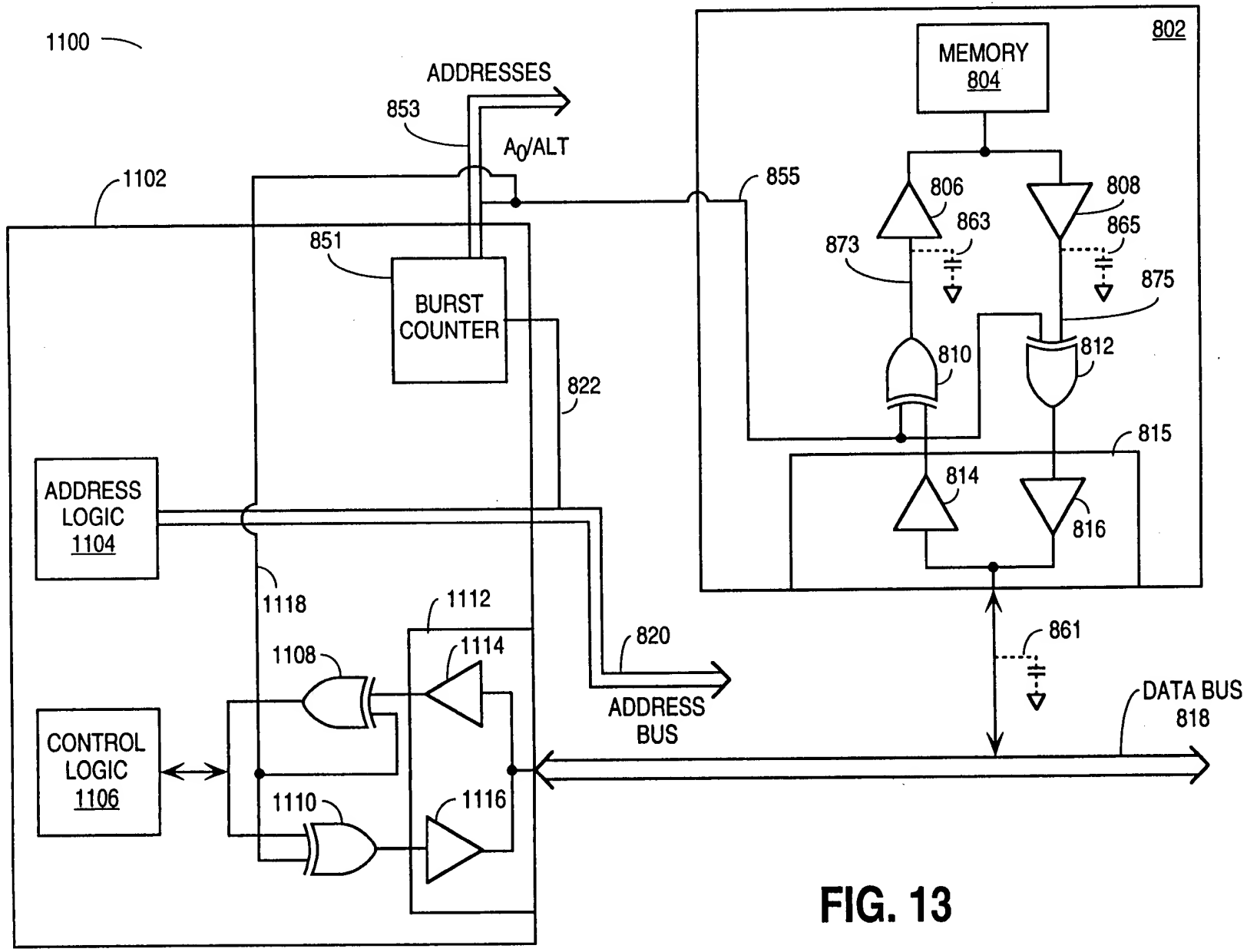


FIG. 13

TO2080" 958h2660

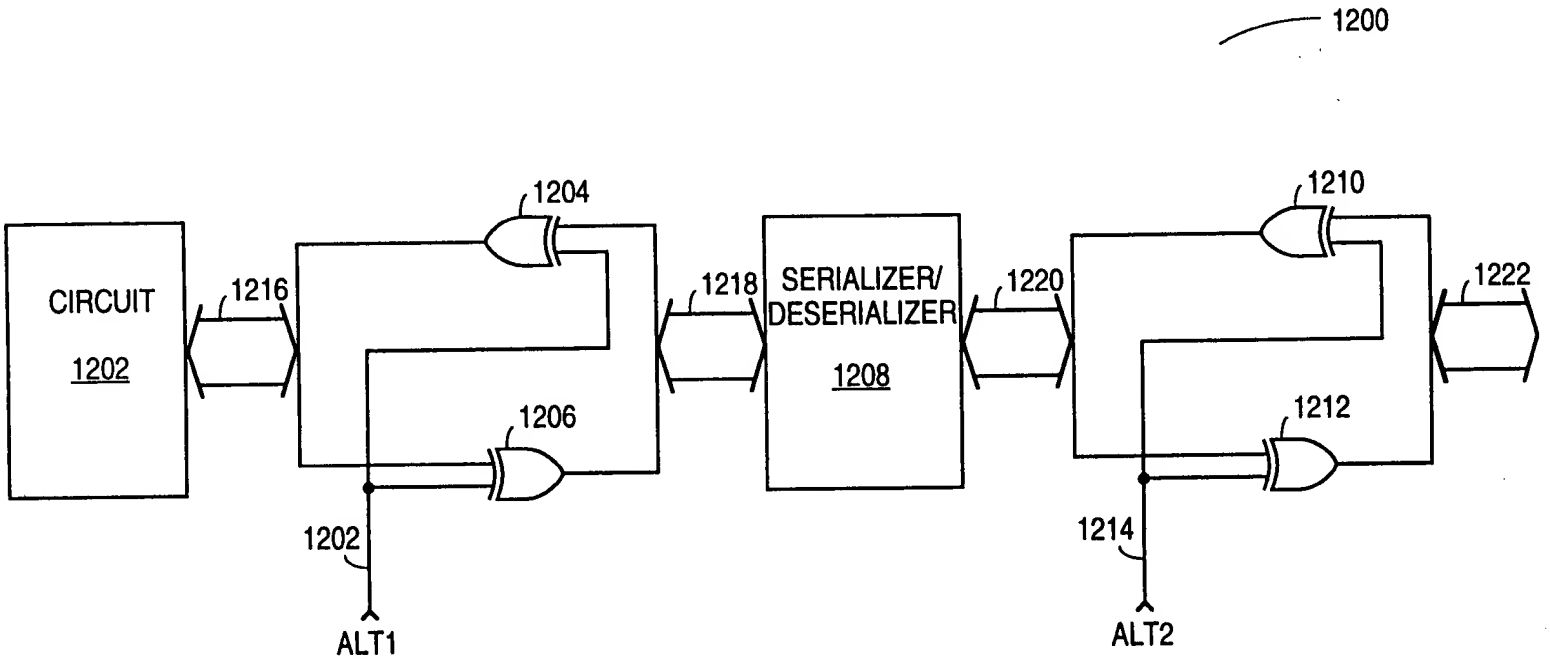


FIG. 14

09524656 - 080704

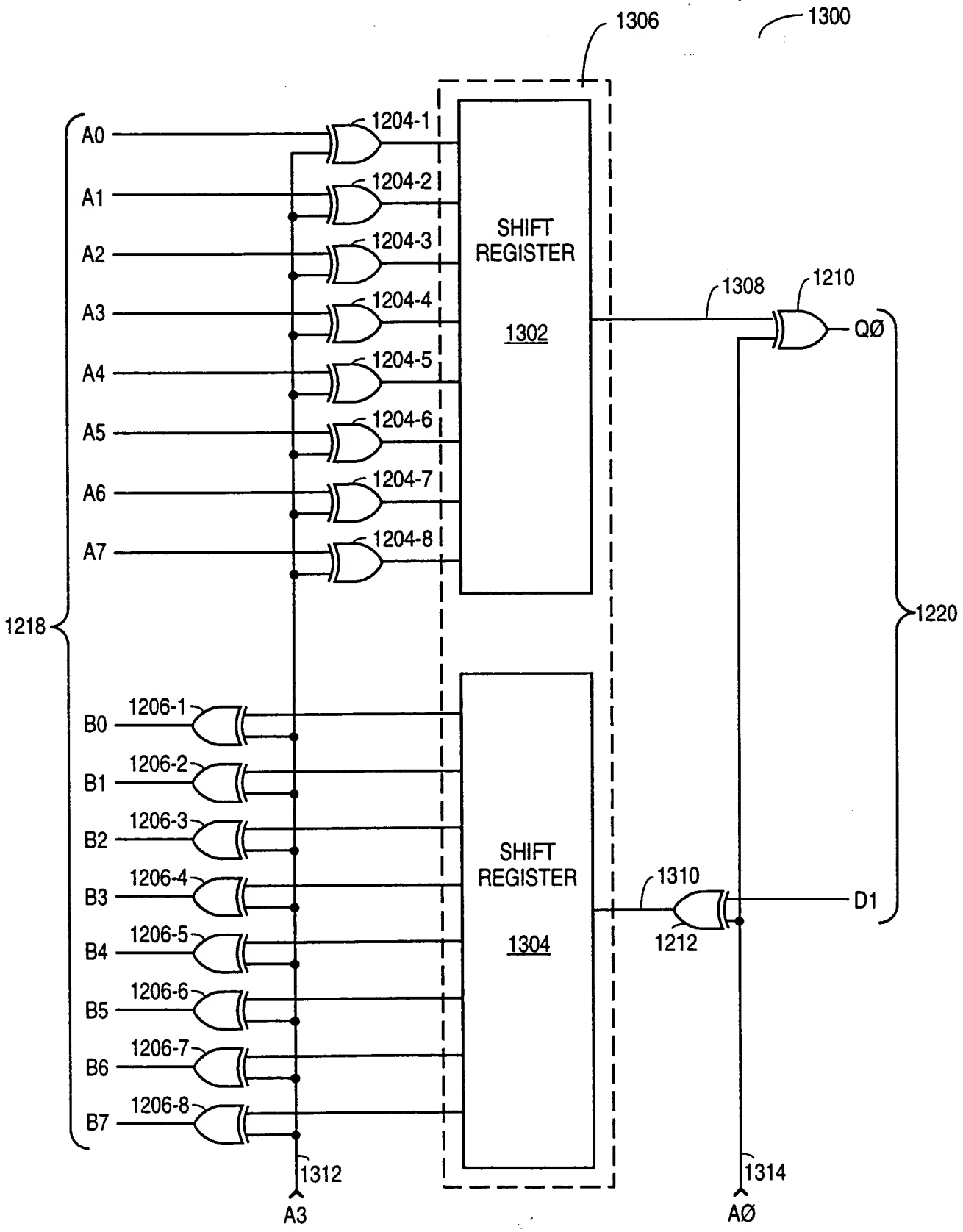


FIG. 15

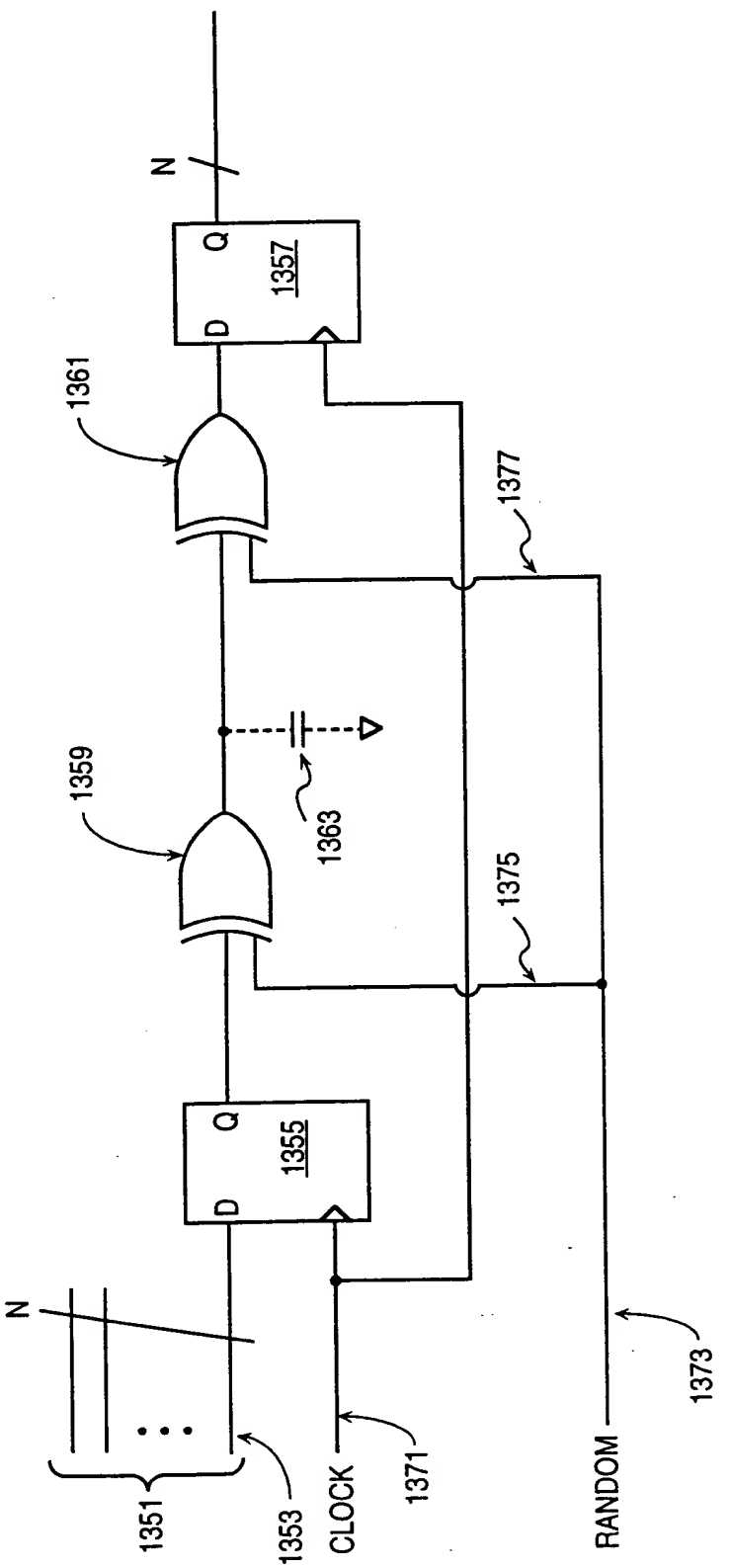

1350
N
Q
D
1357
1361
1377
1359
1363
1375
Q
D
1355
N
1351
1353
CLOCK
1371
RANDOM
1373

FIG. 16

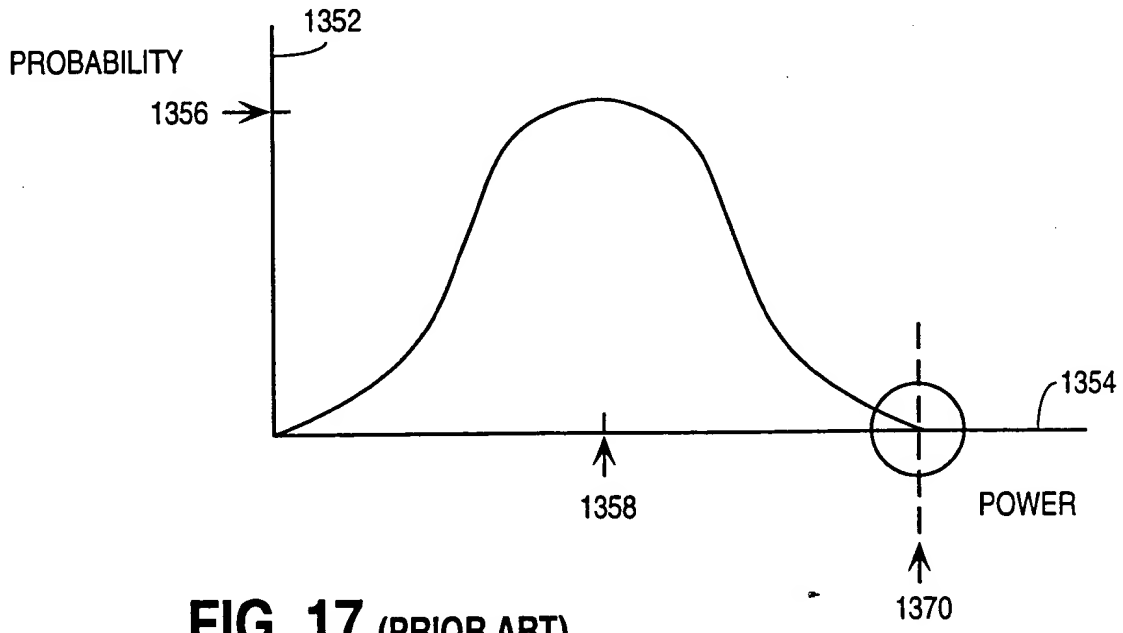


FIG. 17 (PRIOR ART)

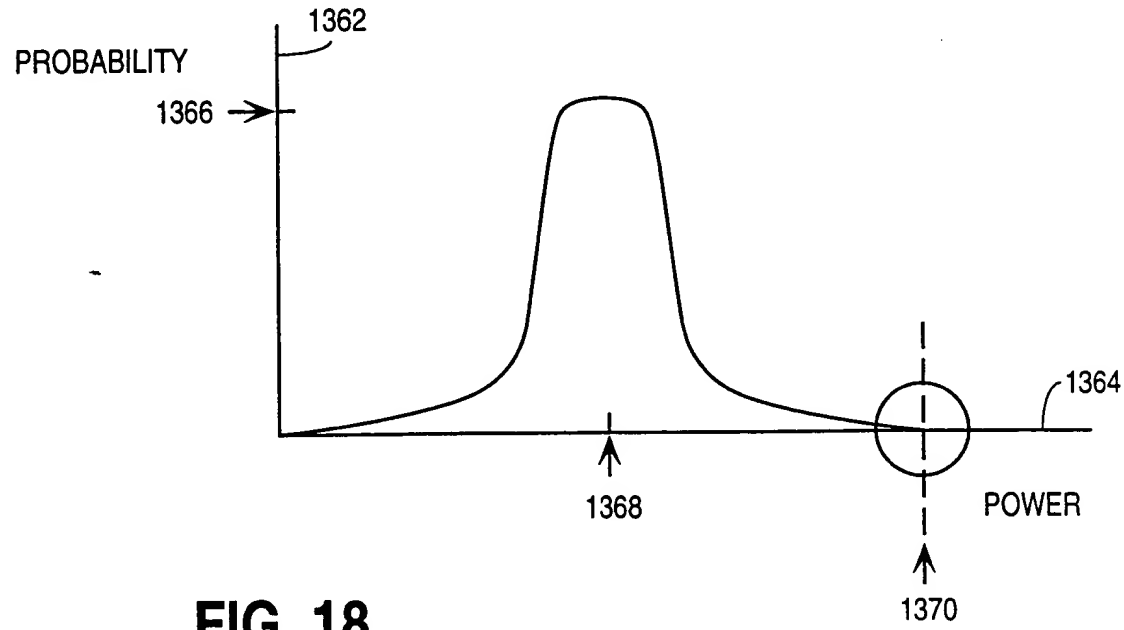


FIG. 18

09245856.060701

T07080" 958n2660

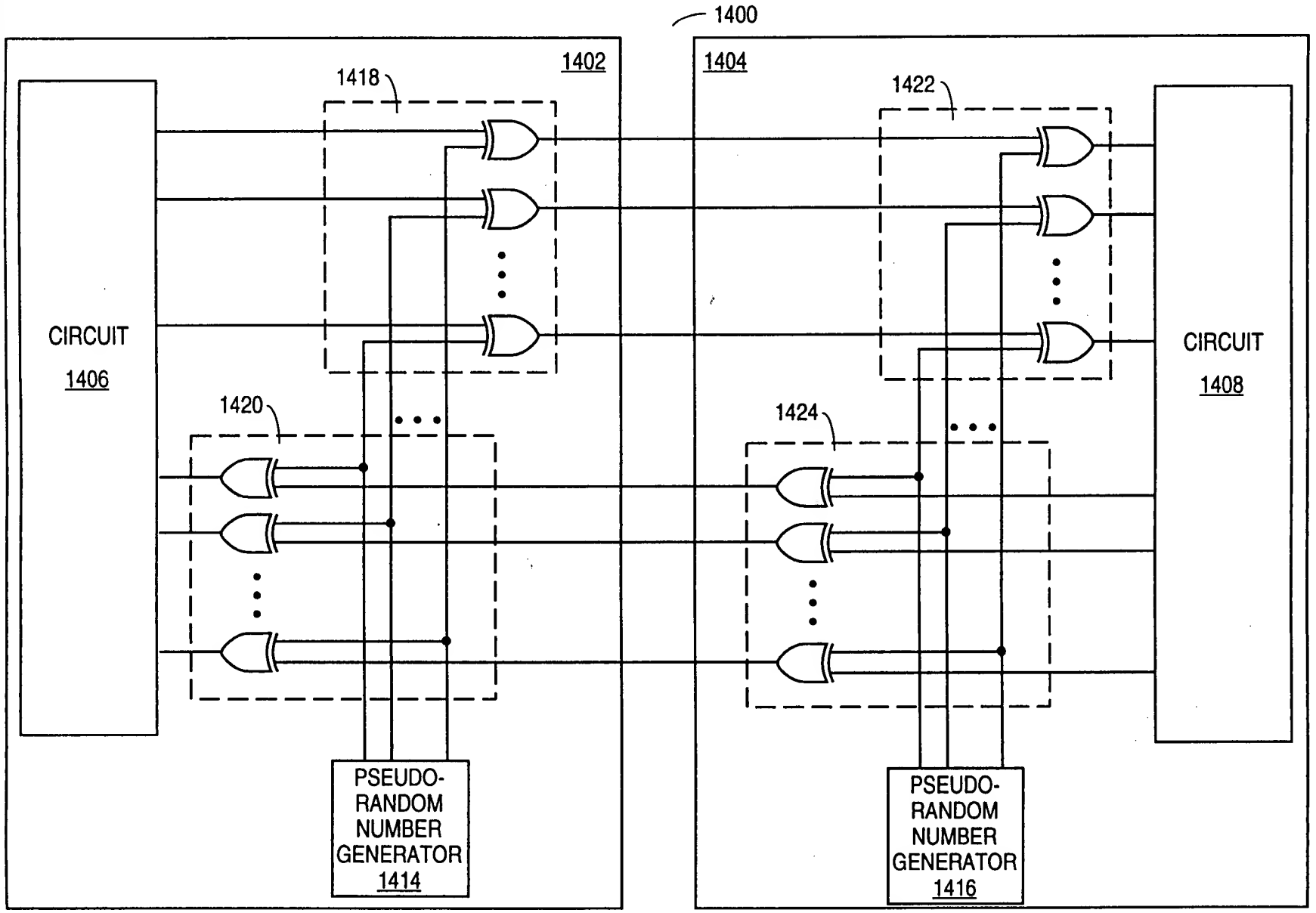


FIG. 19